

OpenATE FIO168

Key Features

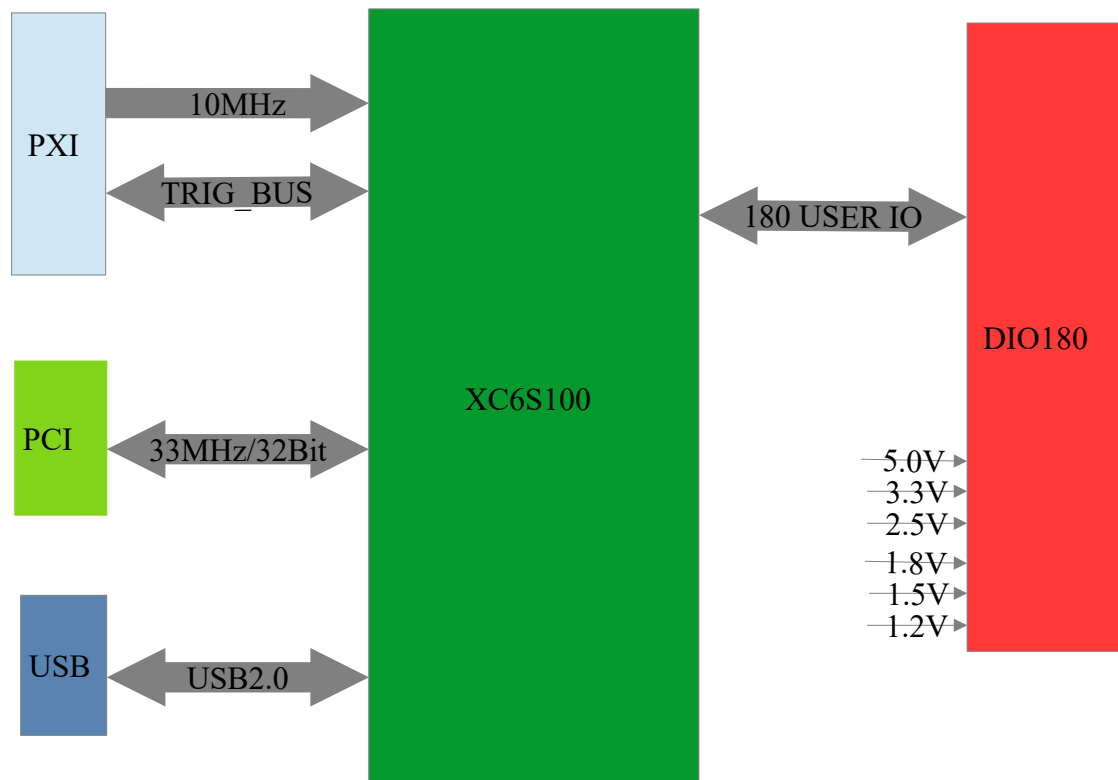
- PCI 32 bit / 33MHz I/O /USB2.0
- PXI Trigger bus 0...7
- Digital I/O 180 pin
1.2V, 1.5V, 1.8V, 2.5V, 3.3V, 5.0V Select-able Level
- Xilinx FPGA Spartan-6 6SLX100GG676-2



Overview

Using the OpenATE USB/ PXI FPGA Carrier, you can develop your design to define custom I/O and control hardware without prior knowledge PCI I/O and USB interface. We can also support customization.

USB/ PXI FPGA Carrier Architectures



OpenATE Inc.

The Open Solution for IC Tester

5F17, 5F., No.5, Sec.5, Xinyi Rd., Taipei City, Taiwan
Tel:886-2-2729-1308; Fax:886-2-2729-1387; www.openate.com

Copyright © OpenATE Inc., All rights reserved. Specifications subject to change without notice.